

J Series® LED Reliability Overview

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INTRODUCTION

This application note details the pre-release qualification testing for J Series® LEDs. The pre-release qualification test suite is based on standard semiconductor pre-release qualification test conditions and methods defined by the Joint Electron Device Engineering Council (JEDEC).

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PRE-RELEASE QUALIFICATION TEST LIST (OPERATING LIFE TESTS)

Test	Applicable Standards	Test Conditions & Failure Criteria
High-Temperature Operating Life Test (HTOL)	JESD22 Method A108-D	Test Conditions: <ul style="list-style-type: none"> Solder point temperature : Maximum in data sheet Forward current : Maximum in data sheet Test period : 1008 hours Failure Criteria ¹: <ul style="list-style-type: none"> Forward voltage shift ² : > 5% Luminous flux degradation ² : > 15% Catastrophic Failure ³
Wet High Temperature Operating Life Test (WHTOL)	JESD22 Method A101-C	Test Conditions: <ul style="list-style-type: none"> Solder point temperature : 60 °C Relative humidity : 90% Forward current : Maximum in data sheet Test period : 1008 hours Failure Criteria ¹: <ul style="list-style-type: none"> Forward voltage shift ² : > 5% Luminous flux degradation ² : > 15% Catastrophic failure ³
Low Temperature Operating Life Test (LTOL)	JESD22 Method A108-C	Test Conditions: <ul style="list-style-type: none"> Ambient Temperature : -40 °C Forward Current : Nominal in data sheet Test Period : 1008 hours Failure Criteria ¹: <ul style="list-style-type: none"> Forward Voltage shift ² : > 5% Luminous Flux degradation ² : > 15% Catastrophic failure ⁵

Notes:

1. The entire test has failed if at least one LED from the sample set satisfies the listed failure criteria. The test is successful if no LED satisfies the listed failure criteria.
2. Comparison is made between [value at time 0] and [value at the end of the test period].
3. A catastrophic failure causes the LED to become non-functional, i.e., open or short.

PROCEDURES FOR OPERATING LIFE TESTS

The following procedures are followed for HTOL and WHTOL tests:

- J Series LEDs are reflow soldered onto metal-core printed-circuit boards (MCPCB).
- MCPCBs are mounted onto heat sinks within reliability test chambers.
- For HTOL and WHTOL tests, solder point temperature (case temperature) is maintained equal to the ambient temperature during the test.
- Power is applied to the lamps. In the WHTOL test, power is applied in one-hour intervals that are followed by one-hour intervals without power to let moisture penetrate the package as much as possible. This procedure results in a test that is more rigorous than one that calls for applying continuous power.
- At regular intervals power is turned off and the sample boards are removed from the tester according to JEDEC testing protocol.
 - ◊ The lamps are characterized according to reliability test criteria.
 - ◊ The boards are placed back into the test chambers and the procedure is repeated until the test has concluded.

- Test period hours are true operating hours, i.e., any time the test chamber is turned off during a test is not counted. This is in compliance with LM-80 procedures.

PRE-RELEASE QUALIFICATION TEST LIST (NON-OPERATING LIFE TESTS)

Test	Applicable Standards	Test Conditions & Failure Criteria
Thermal Shock	JESD22 Method A104-E	Test Conditions: <ul style="list-style-type: none"> • Ambient temperature range : -40 °C to 125 °C • Transfer time : < 20 seconds • Test period : 200 cycles Failure Criteria ¹: <ul style="list-style-type: none"> • LED no longer lights up after test
Mechanical Shock	JESD22 Method B104-C	Test Conditions: <ul style="list-style-type: none"> • Shock : 1500 G • Pulse width : 0.5 ms • Test period : 5 each, 6 axis (30 total) Failure Criteria ¹: <ul style="list-style-type: none"> • LED no longer lights up after test

Note:

1. The entire test has failed if at least one LED from the sample set satisfies the listed failure criteria. The test is successful if no LED satisfies the listed failure criteria.